

Nimish Pradeep Vartak
4733, Aldgate Green, Baltimore MD 21227
Tel. (410) 242-1198 Email: nimishv1@umbc.edu

OBJECTIVE

Seeking to work on challenging projects in an exciting Summer Internship

EDUCATION

Masters of Science (MS) in Computer Science Aug 2004 - Present
University of Maryland, Baltimore County (expected May 2006)

Bachelors of Engineering (B.E.) in Computer Engineering Jun 1999 - May 2003
University of Pune, India GPA: 3.82 (of 4.0)

COMPUTER SKILLS

Languages: C, C++, Assembly 8085/86, Lisp, JavaScript

Protocols: IPv4, IPv6, TCP/IP suite, IGMP, Multicast Listener Discovery (MLD)
MPLS, Ethernet over MPLS (EoMPLS), Label Distribution Protocol (LDP)

Tools: CVS, VSS, Ethereal Packet Analyzer, GDB, UPS

OS: FreeBSD, Linux, Windows 9X, Windows XP

Hardware: Ethernet Hubs, Switches

WORK EXPERIENCE

Zensar Technologies Ltd, India Jul 2003 to Jul 2004
Software Developer at Fujitsu ODC

Projects

Multicast Listener Discovery for IPv6 Apr 2004 to Jul 2004
Developed a test suite for evaluating device performance for Multicast traffic using MLD in IPv6.

MPLS-VPN project Sep 2003 to Mar 2004
This project involved the simulation of a large scale network for evaluating the device performance for EoMPLS using LDP.

'Koala' MPLS software router Nov 2002 to May 2003
This project involved enabling a Linux box to function as an MPLS enabled router, with implementation of LDP, and using RIP as a routing protocol. Final year Bachelors project.

RELEVANT COURSES

Graduate: Advanced Computer Architecture, Advanced Networks and Protocols, Multimedia and Networks

Under-Graduate: Design and Analysis of Algorithms, Computer Networks, Operating Systems, Discrete Structures and Graph Theory, Database Management Systems, Object Oriented Modeling and Design.

SEMINARS & PRESENTATIONS

Seminar on IPv6 delivered to the networking team of Fujitsu ODC Jan 2004

Paper entitled "MPLS - A convergence technology", presented at IEEE Conference at MAE Alandi, India under the guidance of Asst. Professor. R.B Ingle. Apr 2003

Paper entitled "Computational Aspects of Magic Squares found in Sanskrit Texts", presented under the guidance of Professor. C.S.Warnekar. Dec 2002

Seminar on Pseudo Wire Emulation (PWE3), at Dept. of Computer Engg., Pune Institute of Computer Technology (PICT), India Nov 2002

ACADEMIC HONORS

Ranked among top 5% during Bachelors program. Jun 1999-May 2003

Ranked 13th at the twelfth grade exams from about 100,000 students in the Pune Division. April 1998- March 1999

REFERENCES

Available upon request.

ACTIVITIES

Member, organizing committee, "Concepts 2002": Annual national competition of final year student projects in Computer Science, Electronics and Instrumentation departments